

**In the Drawings**

Please replace Drawing sheets 1-9 with Replacement Sheets 1-7.

### **Remarks**

Claims 1-9 and 11 are currently pending in the present application. Claims 6 and 11 have been cancelled. Claim 9 has been amended to correct informalities. The Drawings have been revised as further discussed below. The Specification has been revised in accordance with the Drawing revisions. The Applicant submits that no new subject matter has been introduced herein. In view of the foregoing amendments and following remarks, the Applicant submits that the present Application is now in condition for allowance.

### **Objections to the Drawings**

The Applicant acknowledges the objections to the Drawings. Fig. 2A has been revised to include a source attached to the collectors of the isolation channels of the differential amplifiers for the first and second inputs; Fig. 2B has been revised to correct the supply voltage (Vdc) symbol and to correct the connection of Input 1; Figs. 3 and 4 have been omitted; Figs. 5A-5I have been renumbered as Figs. 3A-3I; and Fig. 6 has been renumbered as Fig. 4. Accordingly, the Applicant respectfully submits that the Drawings have overcome the objections, and respectfully request withdrawal of the objections.

### **Claims Rejected under 35 U.S.C. §112**

The Applicants acknowledge the rejection of Claims 1-8 and 11 under 35 U.S.C. §112. The Applicant respectfully submits that the rejection is now moot as it applies to Claims 6 and 11 in view of their cancellation. Claims 1-5, 7 and 8, however, have been amended to clarify the subject matter being claimed. Accordingly, the Applicant respectfully submits that Claims 1-5, 7, and 8 are now in compliance with §112. Withdrawal of the rejection is respectfully requested.

### **Claims Rejected under 35 U.S.C. §102**

The Applicants acknowledge the rejection of claims 1, 3, 4, 6, 7, and 9 under 35 U.S.C.

§102 as being anticipated under U.S. Patent No. 5,396,131 to Miki et al., hereinafter referred to as "Miki". The Applicant respectfully submits that the rejection is now moot as it applies to Claim 6 in view of its cancellation. The Applicant respectfully submits that Claims 1, 3, 4, 7, and 9 are fully patentable over the Miki for the reasons set forth below.

Claim 1, as amended, recites:

1. A switch circuit comprising:  
a first circuit portion corresponding to a first input port;  
a second circuit portion corresponding to a second input port; and  
an output port,  
wherein each of the first and second circuit portions include at least one first transistor providing a portion of an isolation channel, at least one second transistor providing a portion of a transmit channel, and at least two third transistors for providing a control bias for selecting either the transmit channel or the isolation channel; and  
wherein each third transistor of the first circuit portion is coupled at its base to a base of a corresponding third transistor of the second circuit portion, and to a control voltage source.  
(emphasis added).

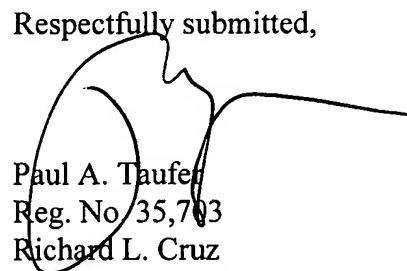
As emphasized above, Claim 1 is directed to a switch circuit wherein each biasing transistor of the first circuit portion is coupled at its base to a base of a corresponding biasing transistor of the second circuit portion, and to a control voltage source. Support for this feature may be found, for example, at paragraph [0026] of the Specification and in Figs. 2A and 2B. As explained in the specification, such coupling allows a control voltage source to control the voltage applied to a bias transistor from each circuit portion, thereby permitting a signal transmitted from the first input port to appear at the output port, while simultaneously isolating a signal transmitted from the second input port, and vice versa. This simultaneous transmitting/isolation provides the switching function of the switch circuit.

Miki, to the contrary, is directed to a high speed A/D converter. (see Abstract of Miki). The high speed A/D converter of Miki comprises a differential amplifier circuit for comparing an applied analog input voltage difference and an applied reference voltage difference. As illustrated in Fig. 10

of Miki, the differential amplifier circuit 400e fails to disclose switching functionality, including a biasing transistor (305, 306) from a first circuit portion (401) being coupled to a corresponding biasing transistor from a second circuit portion (402), and to a voltage source. Instead, Miki discloses the biasing transistors of each circuit portion being coupled to each other and to a respective constant voltage source (309). (see Fig. 10 of Miki). As a result, voltage applied to a biasing transistor (e.g., 305) in a first circuit portion (e.g., 401) does nothing to bias the second circuit portion (e.g., 402). Accordingly, the Applicant therefore respectfully submits that Claims 1, 3, 4, 7, and 9 are patentable over the Miki. Withdrawal of the §102 rejection is respectfully requested.

In view of the foregoing, the Applicant respectfully submits that the present application, including Claims 1-5 and 7-9, is now in condition for allowance, which is respectfully requested.

Respectfully submitted,



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